PC BASED FOR POWER FAILURE MONITORING

CHAN KOK THAI

This report is submitted in partial fulfillment of the requirements for award of Bachelor of Electronic Engineering (Industrial Electronics) With Honours

Faculty of Electronic and Computer Engineering
Universiti Teknikal Malaysia Melaka

April 2008



UNIVERSTI TEKNIKAL MALAYSIA MELAKA FAKULTI KEJURUTERAAN ELEKTRONIK DAN KEJURUTERAAN KOMPUTER

BORANG PENGESAHAN STATUS LAPORAN PROJEK SARJANA MUDA II

Tajuk Projek : PC-Based For Power Failure Monitoring		
Sesi Pengajian : 2007/2008	3	
Saya CHAN KOK THAI mengaku Perpustakaan dengan syarat-syarat ke	membenarkan Laporan Projek Sarjana Muda ini disimpan di egunaan seperti berikut:	
1. Laporan adalah hakmilik Univer	siti Teknikal Malaysia Melaka.	
2. Perpustakaan dibenarkan membu	aat salinan untuk tujuan pengajian sahaja.	
3. Perpustakaan dibenarkan membu	aat salinan laporan ini sebagai bahan pertukaran antara institusi	
pengajian tinggi.		
4. Sila tandakan ($$):		
SULIT*	(Mengandungi maklumat yang berdarjah keselamatan atau kepentingan Malaysia seperti yang termaktub di dalam AKTA RAHSIA RASMI 1972)	
TERHAD*	(Mengandungi maklumat terhad yang telah ditentukan oleh organisasi/badan di mana penyelidikan dijalankan)	
TIDAK TERHAD		
	Disahkan oleh:	
(TANDATANGAN PENULI	S) (COP DAN TANDATANGAN PENYELIA)	
Alamat Tetap: 1523 Tmn Bersatu, Jln Telok Kechai, 06600 Kuala Kedal Kedah Darulaman	Pensyarah Fakulti Kej Elektronik dan Kej Komputer (FKEKK),	
Tarikh: 7 May 2008	Tarikh: 1 May 2008	

"I hereby declare that this report is the result of my own work except for quotes as cited in the references."

Signature

Author

: CHAN KOK THAI

Date

: 7 May 2008

"I hereby declare that I have read this report and in my opinion this report is sufficient in terms of the scope and quality for the award of bachelor of Electronic Engineering (Industrial Electronics) With Honours."

Signature	. Ohfu
	: MDM. YUSMARNITA BINTI YUSOP
Date	: 7/5/08

Dedicated to my beloved family especially my father and mother, lecturer, and also to all my friends

ACKNOWLEDGEMENT

First and foremost, I would like to praise God for His blessing. He gave me physical and mental strength to carry on my final year project up to completion.

I would like to express gratitude and thanks to my supervisor, Mdm. Yusmarnita Binti Yusop for her support and unfailing patience throughout the duration of the project. Her encouragement and guidance are truly appreciated. Otherwise, this project has not been possible. I have learnt a lot under her guidance, be it practically or theoretically.

In addition to that, I also would like to thanks my lecturers: Mr. Tan Kim See, Mr. Zulhairi bin Othman, Mr. Ahmad Sadhiqin bin Mohd Isira, Mr. Zamree bin Abd. Ghani, and Mr. Zulkanain bin Zainuddin. They are the people who give me a hand during facing the problem.

Other than that, I am also grateful to my all friends who help me and giving me opinion along implementation of this project.

I would like to thanks my parent on their moral support as I can count on them whenever I am upset or down.

Finally, I would like to offer thanks and deepest gratitude from the bottom of my heart for all the support, encouragement and inspirations I obtained throughout the duration of this project. The help rendered to me priceless, be it from the smallest of its kind to the largest.

ABSTRACT

PC-based for power failure monitoring is a system created to monitor the parameters of voltage, current, and wattage on the screen of PC. The measured data can be stored in the memory of the PC to allow subsequent downloading and analysis. Hence, the data can be analyzed by other software to detect and predict fault conditions. Besides, the system also can detect the faulty power point during the power trip occurred. The unstable voltage of main power supply and power trip are the phenomenon which mostly happen in the laboratory, and it not only subsequently causing the damage to sensitive equipments, but also affect learning process of students during carry out the experiment. The objectives of this system are to record the measured voltage level for analysis and provides auto detection for the faulty power point during power trip. In order to achieve the project objectives, LabVIEW have been used to create a front panel of virtual instrument. Therefore, the parameters of voltage, current, power, and indicator for faulty power point can be display on the screen of PC for monitoring purpose, and all power points also can be controlled through the front panel. Apart from that, data acquisition module is used for real time monitoring and it provides the interfacing between hardware and software, and then converts the physical value to the readable data by front panel. For future application, this system would be applied to laboratory and it will benefit to students as well as upgrade the learning quality of university.

ABSTRAK

PC-based for power failure monitoring merupakan satu sistem yang dicipta untuk mengawas parameter seperti: voltan, arus, dan kuasa pada paparan komputer. Data yang diukur akan disimpan dalam memori komputer untuk tujuan muat turun dan analisis. Maka, data tersebut boleh dianalisis dengan menggunakan perisian tertentu untuk tujuan mngesan dan meramal keadaan kerosakan. Selain daripada itu, sistem ini juga akan mengesan punca kuasa yang bermasalah ketika bekalan terputus. Ketidakstabilan paras voltan dan bekalan terputus merupakan fenomena yang sering berlaku di makmal, dan ia bukan sahaja menyebabkan kerosakan kepada peralatan makmal yang sensitif, tetapi juga menyebabkan pembelajaran pelajar terjejas semasa menjalankan ujikaji. Objektif utama sistem ini adalah untuk merekod paras voltan yang telah diukur untuk dianalisis, dan juga untuk mengesan punca kuasa yang bermasalah ketika bekalan terputus. Sebagai langkah untuk mencapai objektif projek, LabVIEW digunakan sebagai aturcara bergrafik untuk mencipta panel depan alatan maya. Oleh itu, parameter-parameter untuk voltan, arus, kuasa, dan penunjuk untuk bekalan kuasa yang rosak dapat dipaparkan pada komputer untuk tujuan pengawasan, dan semua punca kuasa dapat dikawal melalui komputer. Di samping itu, Data Acquisition Module digunakan untuk pengawasan masa nyata dan ia membolehkan pengantaramuka di antara perisian dan perkakasan supaya nilai-nilai fizikal yang diukur dapat ditukarkan ke bentuk data yang dapat dibaca melalui panel depan. Sebagai aplikasi masa depan, sistem ini akan diaplikasikan di makmal dan ia akan memanfaatkan pelajar dan juga meningkatkan kualiti pembelajaran universiti.

TABLE OF CONTENTS

CHAPTER	TITI	LE .	PAGE
	PRO	JECT TITLE	i
		ANG PENGESAHAN STATUS LAPORAN	ii
		DENT DECLARATION	· iii
		ERVISORY DECLARATION	iv
		ICATION	
		NOWLEDGEMENT	v
			vi
		TRACT	vii
		TRAK	viii
		LE OF CONTENTS	ix
	LIST	T OF TABLES	xiii
	LIST	OF FIGURES	xiv
	LIST	T OF ABBREVIATION	xvii
	LIST	T OF APPENDIX	xviii
I	INTI	RODUCTION	
	1.1	Introduction	1
	1.2	Objective	2
	1.3	Problem Statement	2
	1.4	Scopes Of Work	3
	1.5	Brief Explaination Of Methodology	3
		-	
	1.6	Report Structure	4

II LITERA	ATURI	E REVIEW
-----------	-------	----------

Ш

2.1	muoa	uction	C
2.2	Background Study		
2.3	LabVl	TEW .	11
	2.3.1	LabVIEW Programming	11
		2.3.1.1 Data Flow Programming	12
		2.3.1.2 Graphical Programming	12
	2.3.2	Advantages Of Using LabVIEW	13
2.4	Data A	Acquisiton	16
	2.4.1	NI DAQ USB 6009	16
		2.4.1.1 Main Features Of NI DAQ USB 6009	17
		2.4.1.2 Device Block Diagram	18
		2.4.1.3 I/O Connectors	19
		2.4.1.4 Analog Input Circuitry	20
		2.4.1.5 Analog Output Circitry	21
		2.4.1.6 Digital Input And Output Circuitry	22
	2.4.2	Advantages Of Using NI USB 6009	22
2.5	Transo	ducer	23
	2.5.1	Voltage Transducer (Step-down Tansformer)	23
		2.5.1.1 Basic Priciple	24
		2.5.1.2 Induction law	25
	2.5.2	Current Transducer	25
2.6	Relay		26
	2.6.1	Relay Operation	27
	2.6.2	Pole and Throw	28
2.7	Optoc	oupler	30
PRO	JECT M	METHODOLOGY	
3.1	Introd	uction	31
3.2	Flow	Chart Diagram	31
3.3	Syster	m Block Diagram	34

	3.4	Hardw	are Development	36
		3.4.1	Circuit Design	36
			3.4.1.1 Schematic for Power Supply	36
			3.4.1.2 Schematic for Logic Circuit	37
			3.4.1.3 Schematic for Measurement Circuit	38
			3.4.1.4 Schematic for High Voltage Isolated Circuit	39
		3.4.2	Prototype Design	40
			3.4.2.1 PCB Fabrication	40
			3.4.2.2 Soldering	42
			3.4.2.3 Wiring	44
			3.4.2.4 Control Box Assembly	46
	3.5	Softwa	re Development	47
		3.5.1	Front Panel	48
		3.5.2	Block Diagram	49
		3.5.3	Control and Indicator	50
			3.5.3.1 Control	50
			3.5.3.2 Graph and Chart	52
		3.5.4	Mathematic	52
			3.5.4.1 Formula Node	52
			3.5.4.2 Mathscript Node	53
		3.5.5	Programming	54
			3.5.5.1 For Loop Structure	54
			3.5.5.2 WhileLoop Structure	54
			3.5.5.3 Case Structure	55
		3.5.6	Tools and Shottcut	56
IV	RESU	LT AN	D DISCUSSION	
	4.1	Introdu	uction	59
	4.2	Analys	sis of Overall System	59
	4.3	Softwa	are Analysis	60
		4.3.1	Analysis of Front Panel	60
		4.3.2	Analysis of Block Diagram	63

C Universiti Teknikal Malaysia Melaka

			xii
		4.3.2.1 Sub Program of Voltage and	
		Current Acquisition	63
		4.3.2.2 Sub Program of Power Conditioning and	
		Controlling	64
		4.3.2.3 Sun Program of Recording	65
4.4	Analy	sis of Hardware	68
	4.4.1	General Circuit	68
	4.4.2	Analysis of Voltage Transducer	69
	4.4.3	Analysis of Current Transducer	71
	4.4.4	Analysis of Failure Detector Circuit	72
4.5	Testin	g and Troubleshooting	74
CON	CLUSI	ON AND RECOMMENDATION	
5.1	Concl	usion	79
5.2	Recor	mmendation	80

81

REFERENCE

LIST OF TABLES

NO	TITLE	PAGE
2.1	Analog Terminal Assignment	19
2.2	Digital Terminal Assignment	20
3.1	Color Coding For Industrial Control Panels.	44
3.2	Tools and Shortcuts	56
4.1	Programming Code of Sub Program 1	64
4.2	Programming Code of Sub Program 2	65
4.3	Programming Code of Sub Program 3	66
4.4	Recorded Text Data	66
4.5	Measured and Acquired Values	70

LIST OF FIGURES

NO	TITLE	PAGE
2.1	Voltage Surge	7
2.2	Spike	8
2.3	Voltage Sag	8
2.4	Noise	9
2.5	Momentary Interruption	10
2.6	Source and Types Of power Quality Disturbance	10
2.7	Most-Used Software for Data Acquisition and Instrument Control	15
2.8	NI DAQ USB 6009	16
2.9	Devise Block Diagram NI USB 6009	18
2.10	Analog input Circuitry	20
2.11	Analog Output Circuitry	21
2.12	Example of Connecting A load	22
2.13	Step-Down transformer	23
2.14	Working Principle of Step-Down Transformer	24
2.15	Current Transducer	26
2.16	Relay	27
2.17	Circuit Symbol of Relay	28
2.18	Optocoupler	30
2.19	Circuit Symbol of Optocoupler	30
3.1	Flow Chart Diagram	31
3.2	System Block Diagram	34
3.3	Schematic of Power Supply	37
3.4	Schematic of Logic Circuit	38
3.5	Schematic of Measurement Circuit	38

3.6	Schematic of High Voltage Isolated Circuit	39
3.7	PCB Layout of Power Supply Circuit	40
3.8	PCB Layout of Measurement Circuit	41
3.9	PCB Layout of Logic Circuit	41
3.10	Soldered PCB of Power Supply Module	42
3.11	Soldered PCB of Logic Module	43
3.12	Soldered PCB of Measurement Module	43
3.13	Soldered PCB of Relay Module	43
3.14	Soldered PCB of High Voltage Isolated Module	44
3.15	Wiring of Control Box	46
3.16	Casing of Control Box	47
3.17	Window of Front Panel	48
3.18	Window of Block Diagram	49
3.19	Basic Control	51
3.20	Waveform Graph	52
3.21	An Example of a Mathematical Operation Using Operator Nodes	53
3.22	Formula Node	53
3.23	Math Script Node	53
3.24	For Loop Structure	54
3.25	While Loop Structure	55
3.26	Case Structure	56
4.1	Front Panel 1 of Virtual Instrument	61
4.2	Front Panel 2 of Virtual Instrument	61
4.3	Sub Program of Voltage and Current Acquisition	63
4.4	Sub Program of Power Conditioning	64
4.5	Sub Program of Recording	65
4.6	Graphical Programming of Overall System	66
4.7	General Circuit	68
4.8	Voltage Transducer Circuit	69
4.9	Ladder Diagram of Failure Detection	73
4.10	Relay and MCB Problem	74
4.11	Proper Connection of LTS 6-NP	75
4.12	Re-arranged Components in Control Box	76
4.13	Replaced Power Relay	76

		XVI
4.14	Stainless Steel Plate	76
4.15	Steel cover and Aluminum Plate	77
4.16	Current Limiter Circuit	77
4.17	High Wattage Resistor	77
4.18	Improved Current Limiter Circuit	78

LIST OF ABBREVIATION

PC - Personal Computer

VI - Virtual Instrument

LabVIEW - Laboratory Virtual Instrumentation Engineering

Workbench

DAQ - Data Acquisition

NI - National Instrument

AC - Alternating Current

EMI - Electro-Magnetic Interference

RFI - Radio Frequency Interference

ADC - Analog-To-Digital Converter

MUX - Multiplexer

PGA - Programmable Gain Amplifier

NO - Normally Open

NC - Normally Closed

SMS - Short Message Service

LIST OF APPENDIX

NO	TITLE	PAGE
A	LabVIEW Reference Guide	82
В	Data Sheet NI USB 6009	83
C	Datasheet Current Transducer LTS 6-NP	85
D	Datasheet Optocoupler	87

CHAPTER 1

INTRODUCTION

1.1 Introduction

In recent years, with the extensive uses of the personal computer (PC), the interest with the heuristic applications both on the software tools for the development of virtual instruments and the tools for the computer interconnection at great distance had increased steadily. This interest is mainly due to the cost of the experimental laboratories can be reduced and the speed of development can be accelerated. A virtual measurement system which is the most widely used for power systems have been introduced to simplify the design, implementation, and use of programmable measurement system by adopting a visual interface.

This system titled "PC-based for power failure monitoring" is using the LabVIEW package has appeared on the market to enables the creation of "Virtual Instruments" which can display on the screen of a PC to measured quantities of voltage, current, and power. The use of a PC-based metering scheme gives rise to numerous added benefits; the measured data can be stored in the memory of the PC to allow subsequent downloading and analysis; the data can be analyzed by other software to detect and predict fault conditions. This system also performs an addition condition monitoring function for each power point in the laboratory which it can monitor the operating power point, and also detect the faulty Power Point when power trip occurred. In normal operating condition, this system can give the warning

to the user by turn on the buzzer and light up the abnormal voltage indicator in the virtual instrument when the voltage level exceed the set limit of normal voltage. The person in-charge also can turn OFF all the power point in the laboratory in one time through the front Panel of Virtual Instrument on the screen of PC if he or she needs to leave the laboratory in emergency case. For further development, this system also cans SMS to the person in-charge when abnormal of voltage condition occurred.

1.2 Objectives

The main purposed of this project are to design and implement a PC-based for power failure monitoring system. Therefore, the objectives as below should be achieved.

- 1. To design and develop the PC-based for power failure monitoring system by using LabVIEW.
- 2. To study and understand the basic concept of voltage and current measurement.
- 3. To identify the suitable type of DAQ and transducers in the project, and also study the interfacing technique between software and control circuit.
- 4. To store the measured voltage record for analysis, detect faulty power point when power trip occurred, monitor the operated power point, and to close all the power point in one time through the front panel.

1.3 Problem Statement

Power quality, in recent years, has become an important issue and is receiving increasing attention by utility, facility, and consulting engineers. Present equipment setups and devices used in commercial and industrial facilities, such as digital computers, power electronic devices, and automated equipment, are sensitive to many types of power disturbances. Power disturbances arising within facilities have increased significantly due to the increasing use of energy efficient equipment such as switch-mode power supplies, inverters for variable speed drives, and more.

The monitoring and data collection of power disturbances for power quality study therefore has to be conducted. In the other side, the situation of power trip is one of the phenomenons that always happen in the laboratory. This because of the short circuit occurred during students carry out the experiment because of wrong circuit construction. This would cause the time wasting for find the faulty power point and student are unable to complete their task within time given. This will caused the bad learning effect to the students. Besides that, the careless of students have been an issue related to safety aspect of laboratory. Some of the students are not turn OFF the equipments after used for lab session. These not only waste the electricity and shorten the life of equipments, but also expose to risk of fire due to equipment over heat. This project is designed and developed to overcome these problems.

1.4 Scope of Work

The scopes of works in this project are:

- 1. LabVIEW is used to create the front panel of virtual instrument.
- 2. NI DAQ 6009 is a data acquisition unit that used to interfacing between hardware and software.
- 3. Step-down transformer is used to step down the AC voltage from power terminal to the voltage level that can read by data acquisition unit.
- 4. Current Transducer is used to converts the measured load current to the voltage level that can be use by data acquisition unit.
- 5. Relay and control circuit are used to control and detect the condition of power point.

1.5 Brief Explanation of Methodology

First of all, this project is beginning by having a discussion with supervisor about the general ideas and concepts that would be used in this project. Next, for literature review stage, the background of this project is studied and research is done by referring various sources like: reference book, I.E.E.E journals, website of National Instrument, and data sheet. For the following stage, all the information

related to components, DAQ, transducer is seeking, and the most suitable would be selected for used in this project. On next stage, the LabVIEW programming is studied, and the front panel of virtual instrument is created and simulated. Hardware interfacing would be studied on the following stage. After that, the hardware for this project is built and assembled; and the system is ready for overall system testing. If the outputs of this system fulfill the project requirements and specification, so this project is considered success. If the output of this system did not fulfill the desired output, so the troubleshooting would be carry out until it reaches the project requirements.

1.6 Report Structure

This report is documentary delivering the ideas generated, concepts applied, activities done, and finally the product of project itself. It consists of five chapters. Following is a chapter-by-chapter description of information in this report.

Chapter 1 gives reader a basic introduction to how the idea of this project generated. The chapter contains introduction, objective of the project, problem statement, scopes of work, brief methodology, and report structure.

Chapter 2 is a literature review on theoretical concepts applied in this project. The chapter concludes the background study of power system, PC-based monitoring system. Besides that, this chapter also explains how the PC-based for power failure monitoring work, what is LabVIEW, what is DAQ, and application of others component. Then, why choose the specific DAQ, transducer, and related components.

Chapter 3 introduces the methodology of the project. The chapter contains the flow chart which explains the overall method taken along the project carry out. Besides that, this chapter also introduces the construction of the project, which involves hardware development and software development. Basically, the hardware development for the project concludes with circuit design, prototype or body design, and PCB fabrication. Besides, the software development of project will discuss what

graphical programming is, how to use the LabVIEW, and how to implement it on this project.

Chapter 4 will be covered all the result from designing process. It will also include a discussion about the project. The chapter concludes with discussion on front panel of virtual instrument and control circuit for the system.

Chapter 5 will be conclusion of the PSM project. The chapter concludes with some recommendation that can be implemented in the future.

CHAPTER II

LITERATURE REVIEW

2.1 Introduction

This chapter discusses about literature discourse and review of PC-Based for Power Failure monitoring system. Throughout the world, there have been many researches about the concept and implementation of this PC-based for power monitoring system. This project is implemented to enhance the available similar system with additional functions and applied it on the Laboratory of Electronic Industrial at UTeM.

Literature reviews are based in information obtained from valid sources such as books, articles of relevance, published paper or any other source deemed appropriate. One of the more famous sources for literature reviews from IEEE, denoting the Institute of Electrical and Electronics Engineers which is based in New York, USA. The forms of literature include standards of practice, proceeding paper or conference papers such as those from the Power Engineering Conference.

2.2 Background Study

Power Quality is a technical term that has practical implications for equipment. When power is generated, it has very predictable characteristics. It